# -500mA / -50V Digital transistors (with built-in resistors)

# **DTB123EK / DTB123ES**

# Applications

Inverter, Interface, Driver

#### Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

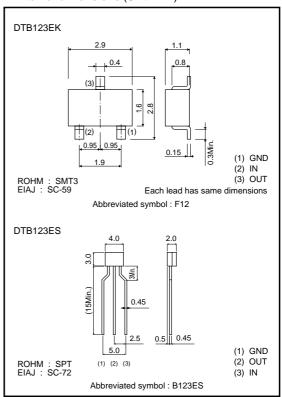
#### Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

Packaging specifications

	Package	SMT3	SPT	
	Packaging type	Taping	Taping	
	Code	T146	TP	
Part No.	Basic ordering unit (pieces)	3000	5000	
DTB123EK		0	_	
DTB123ES		_	0	

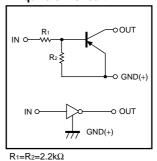
#### ●External dimensions (Unit : mm)



#### ● Absolute maximum ratings (Ta=25°C)

<b>_</b>							
Parameter	Cumbal	Limits		Unit			
	Symbol	DTB123EK DTB123ES					
Supply voltage	Vcc	-50		V			
Input voltage	Vin	−12 t	V				
Output current	Ic	-500		mA			
Power dissipation	Po	200 300		mW			
Junction temperature	Tj	150		ဗ			
Storage temperature	Tstg	-55 to +150		င			

# ●Equivalent circuit



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# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	_	_	-0.5	V	Vcc= -5V, Io= -100μA
	VI(on)	-3	_	_		Vo= -0.3V, Io= -20mA
Output voltage	Vo(on)	_	-0.1	-0.3	V	lo/l=-50mA/-2.5mA
Input current	lı	_	_	-3.8	mA	V≔-5V
Output current	IO(off)	_	_	-0.5	μΑ	Vcc= -50V, V⊫0V
DC current gain	Gı	39	_		_	Vo= -5V, Io= -50mA
Input resistance	R <sub>1</sub>	1.54	2.2	2.86	kΩ	-
Resistance ratio	R2/R1	0.8	1	1.2	_	-
Transition frequency	f⊤ *	_	200	_	MHz	Vc=-10V, I= 50mA, f= 100MHz

<sup>\*</sup> Characteristics of built-in transistor

# •Electrical characteristic curves

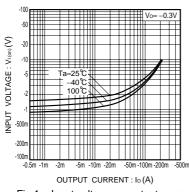
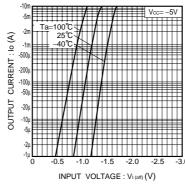


Fig.1 Input voltage vs. output current (ON characteristics)



INPUT VOLTAGE: VI (off) (V)

Fig.2 Output current vs. input volt
 (OFF characteristics)

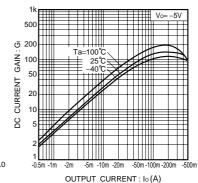


Fig.2 Output current vs. input voltage Fig.3 DC current gain vs. output current (OFF characteristics)

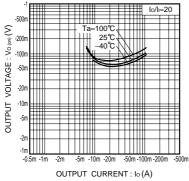


Fig.4 Output voltage vs. output current

Rev.B

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